

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventors: Katsuhiko TSUURA

Art Unit 2818

Appln. No.: 10/673,479

Exr. D. NHU

Filed: September 30, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF
PRODUCING THE SAME



RESPONSE UNDER 37 CFR 1.111

Assistant Commissioner of Patents
Washington, DC 20231

Dear Sir:

In response to the office action dated March 24, 2004, reconsideration and allowance are respectfully requested in light of the submission herewith of a terminal disclaimer to overcome the obviousness-type double patenting rejection of claims 21-31 based on claims 1024 of USPN 6,677,195 B2.

Consideration of the Information Disclosure Statement filed September 30, 2003 is requested. A courtesy copy of that Information Disclosure Statement, with proof of filing on September 30, 2003, is attached.

A notice of allowance is respectfully requested.

Respectfully submitted,

James E. Ledbetter
Registration No. 28,732

Date: May 28, 2004

JEL/att

ATTORNEY DOCKET NO. JEL 31098B
STEVENS, DAVIS, MILLER & MOSHER, L.L.P.
1615 L STREET, NW, Suite 850
WASHINGTON, DC 20043-4387
Telephone: (202) 785-0100
Facsimile: (202) 408-5200